

Kazuhiko NAGANO, et al.
METHOD FOR STABILIZING.....
Filing Date: September 29, 2003
202-663-7060
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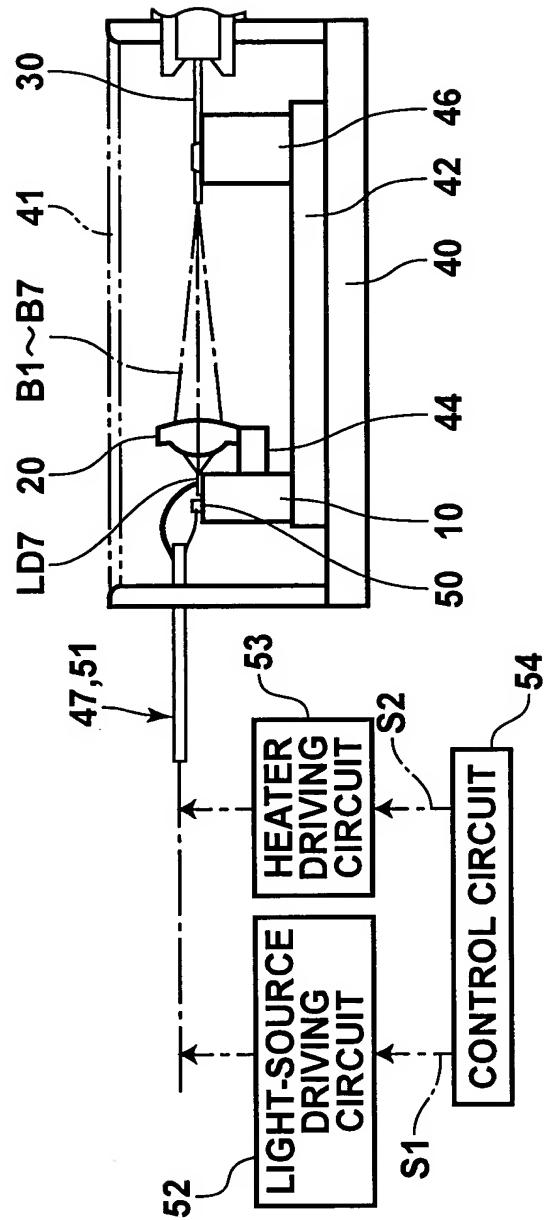


FIG. 1

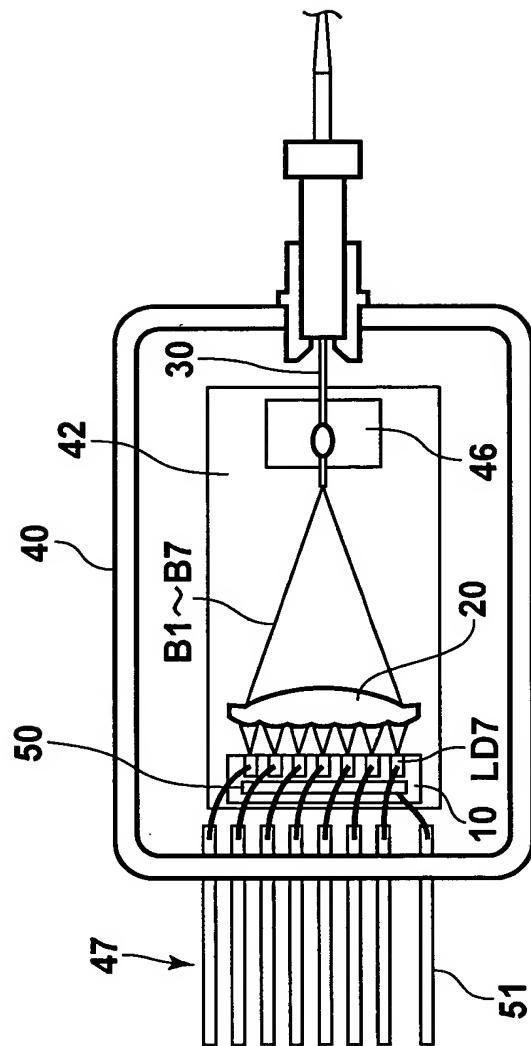


FIG.2

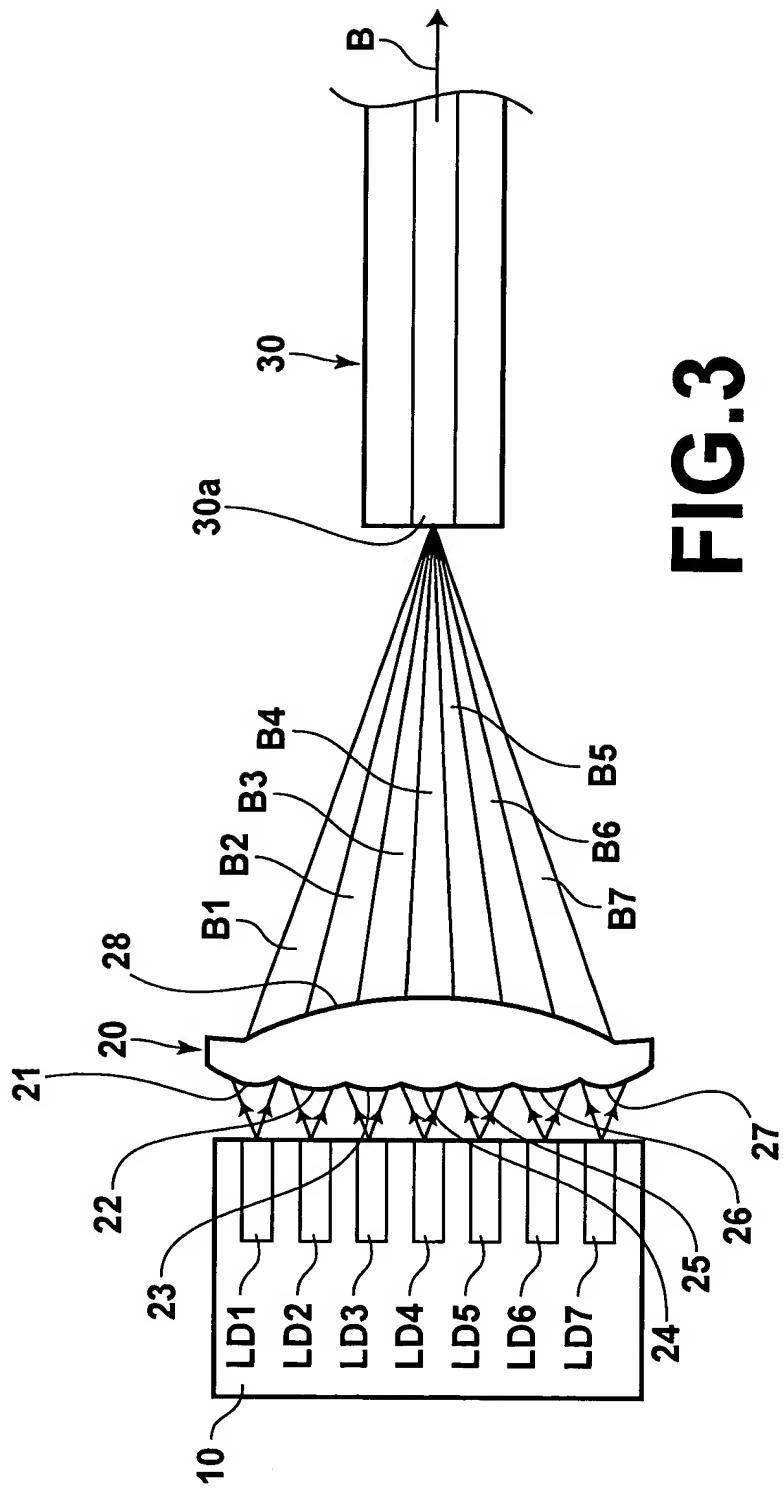


FIG.3

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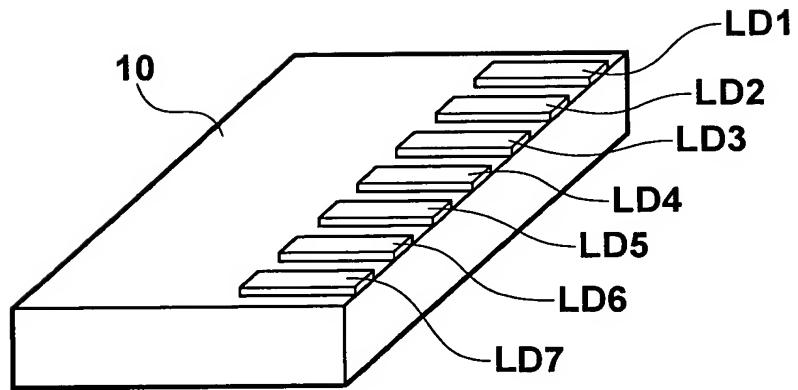


FIG.4

FIG.5A

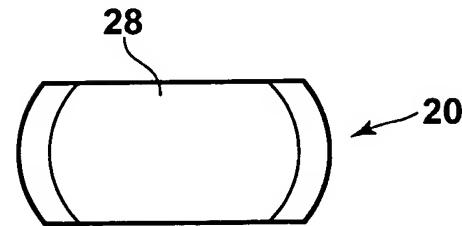


FIG.5B

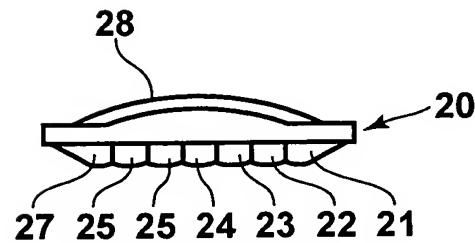


FIG.5C

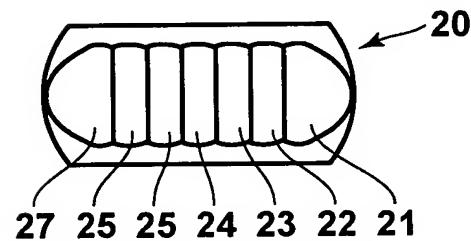
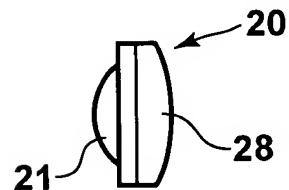
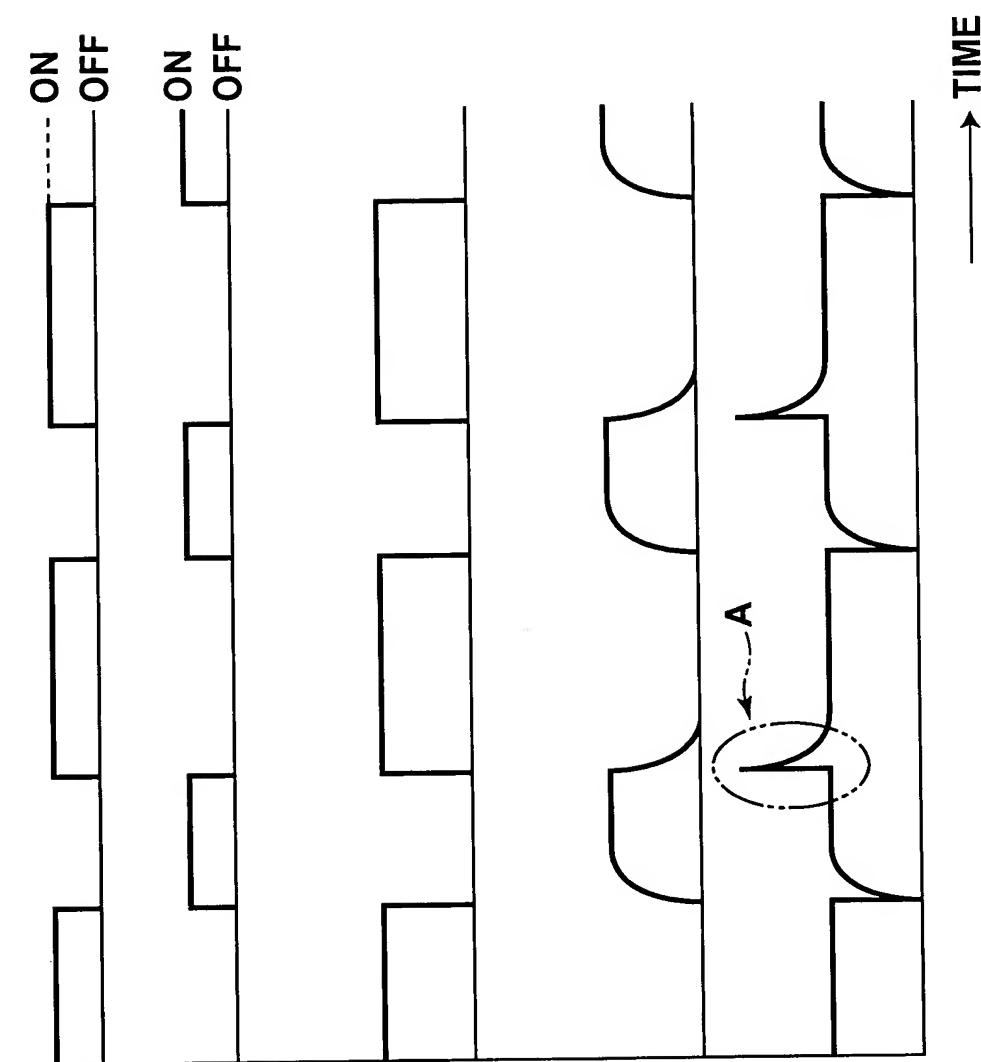


FIG.5D





- (1) VOLTAGE OF LD-DRIVING COMMAND SIGNAL (S1)
- (2) VOLTAGE OF HEATER-DRIVING COMMAND SIGNAL (S2)
- (3) LD CURRENT
- (4) HEATER CURRENT
- (5) SUM OF CURRENTS FLOWING THRU LD AND HEATER

FIG.6

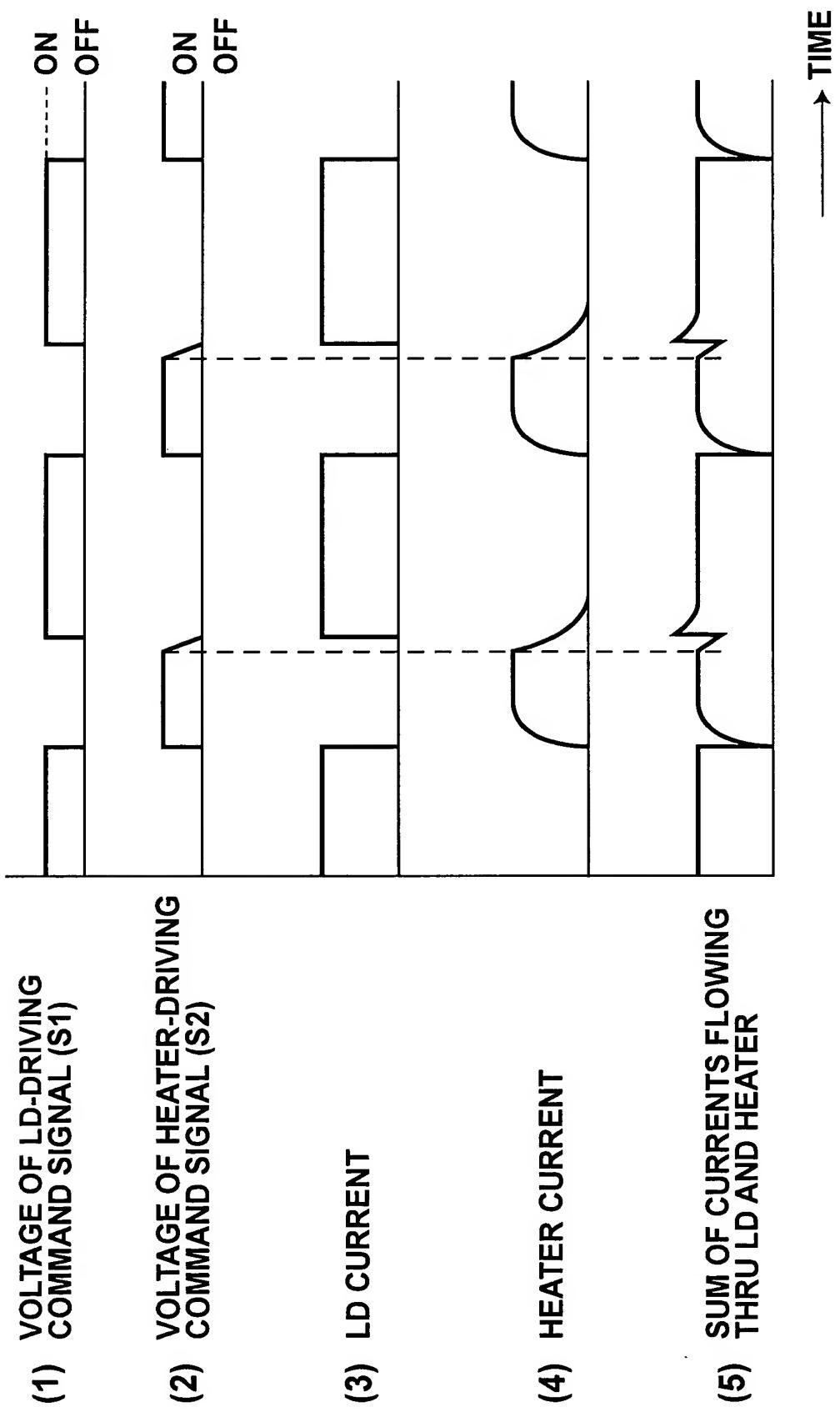


FIG.7